

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Divisional Patent Application of)
Hiroshi YUASA)
Based On Application No. 09/790,518) Art Unit: 2814
Which Was Filed: February 23, 2001) Examiner: A. Mai
For: SEMICONDUCTOR DEVICE AND)
METHOD FOR FABRICATING THE SAME) Date: October 30, 2003

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

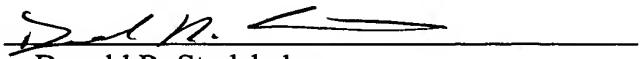
Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. §1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98.

The references listed on the attached Form PTO-1449 were cited in parent application Serial No. 09/790,518 filed February 23, 2001, from which priority is claimed under 35 U.S.C. 120.

It is requested that the accompanying PTO-1449 be considered and made of record in the above-identified application. To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner initialed copy of this form be returned to the undersigned.

Respectfully submitted,

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Substitute for form 1449A/PTO				<i>Complete if Known</i>	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Application Number	New Application
Sheet	1	of	1	Filing Date	October 30, 2003
				First Named Inventor	Hiroshi YUASA
				Art Unit	2814
				Examiner Name	Anh D. Mai
				Attorney Docket Number	740819-1028

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	U.S. Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (<i>if known</i>)	Kind Code ³ (<i>if known</i>)			
		US-6,365,528 B1		04/02/2002	Sukharev et al.	
		US-6,472,755 B1		10/29/2002	Ngo et al.	
		US-6,350,670 B1		02/26/2002	Andedih et al.	
		US-2002/0034874		03/21/2002	Aoki	

FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ⁴ Number ⁴	Kind Code ⁵ (<i>if known</i>)				
		JP 11-87503		03/30/1999	Ohashi et al.		Abstract
		JP 11-297829		10/29/1999	Yokoyama et al.		Abstract
		JP 2000-58642		02/25/2000	Kojima et al.		Abstract
		JP 6-216116		08/05/1994	Sasaki		Abstract
		JP 9-275138		10/21/1997	Matsunou		Abstract
		JP 2002-26121		01/25/2002	Maekawa et al.		Abstract

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS							
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					T ²
		S. Wolf et al., "Silicon Processing for the VLSI Era", Vol. 1, 1986 Lattice Press, p. 184.					

Examiner Signature	Date Considered
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.